## SKM 400GB176D ...



#### Trench IGBT Modules

## **SKM 400GB176D SKM 400GAL176D**

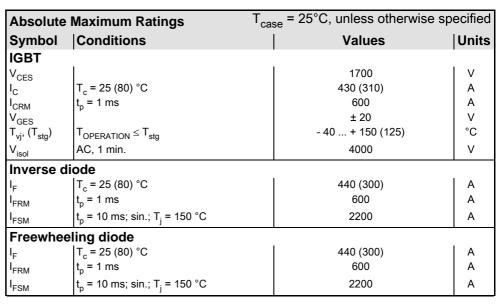
**Preliminary Data** 

#### **Features**

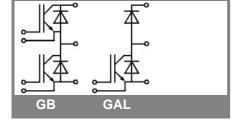
- Homogeneous Si
- Trench = Trenchgate technology
- V<sub>CE(sat)</sub> with positive temperature
- High short circuit capability, self limiting to 6 x I<sub>C</sub>

### **Typical Applications**

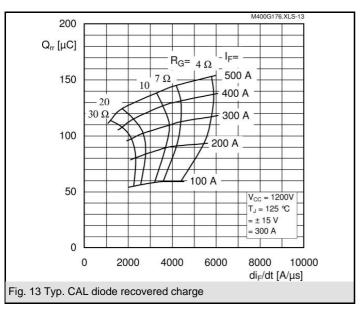
- AC inverter drives
- mains 575 750 V AC
- Public transport (auxiliary syst.)
- Wind power

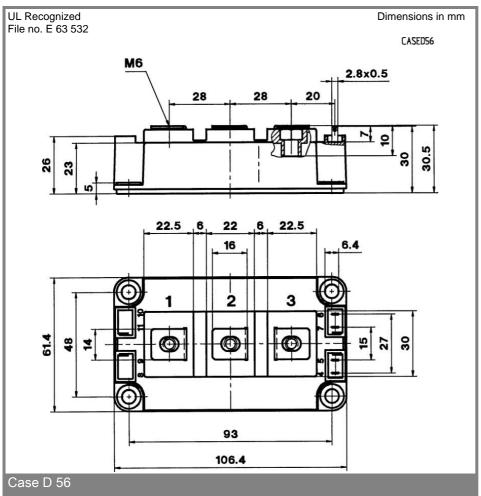


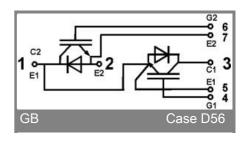
Characteristics		T <sub>case</sub> = 25°C, unless otherwise specified			
Symbol	Conditions	min.	typ.	max.	Units
IGBT	•	•			
$V_{GE(th)}$	$V_{GE} = V_{CE}$ , $I_C = 12 \text{ mA}$	5,2	5,8	6,4	V
I <sub>CES</sub>	$V_{GE} = 0$ , $V_{CE} = V_{CES}$ , $T_j = 25 (125) °C$		0,15	0,45	mA
$V_{CE(TO)}$	T <sub>i</sub> = 25 (125) °C		1 (0,9)	1,2 (1,1)	V
r <sub>CE</sub>	V <sub>GE</sub> = 15 V, T <sub>j</sub> = 25 (125) °C		3,3 (5,2)	4,2 (6)	mΩ
V <sub>CE(sat)</sub>	$I_{Cnom}$ = 300 A, $V_{GE}$ = 15 V, chip level		2 (2,45)	2,4 (2,9)	V
C <sub>ies</sub>	under following conditions		13,2		nF
C <sub>oes</sub>	V <sub>GE</sub> = 0, V <sub>CE</sub> = 25 V, f = 1 MHz		0,6		nF _
C <sub>res</sub>			0,5	00	nF
L <sub>CE</sub>				20	nH
R <sub>CC'+EE'</sub>	res., terminal-chip T <sub>c</sub> = 25 (125) °C		0,35 (0,5)		mΩ
t <sub>d(on)</sub>	V <sub>CC</sub> = 1200 V, I <sub>Cnom</sub> = 300 A		330		ns
t <sub>r</sub>	$R_{Gon} = R_{Goff} = 4 \Omega, T_j = 125 °C$		55		ns
t <sub>d(off)</sub>	V <sub>GE</sub> ± 15 V		880		ns
t <sub>f</sub>			145		ns
E <sub>on</sub> (E <sub>off</sub> )			170 (118)		mJ
Inverse o		•			•
$V_F = V_{EC}$	$I_{Fnom} = 300 \text{ A}; V_{GE} = 0 \text{ V}; T_j = 25 (125)$		1,7 (1,8)	1,9 (2)	V
$V_{(TO)}$	T <sub>j</sub> = 25 (125) °C		1,2 (0,9)	1,4 (1,1)	V
r <sub>T</sub>	T <sub>j</sub> = 25 (125) °C		1,7 (3)	1,7 (3)	mΩ
I <sub>RRM</sub>	$I_{Fnom} = 300 \text{ A}; T_j = 125 \text{ ( ) } ^{\circ}\text{C}$		418		Α
$Q_{rr}$	di/dt = 5800 A/μs		117		μC
E <sub>rr</sub>	V <sub>GE</sub> = 0 V		78		mJ
FWD					
$V_F = V_{EC}$	$I_F = 300 \text{ A}; V_{GE} = 0 \text{ V}, T_j = 25 (125) ^{\circ}\text{C}$		1,7 (1,8)	1,9 (2)	V
$V_{(TO)}$	$T_j = 25 (125) ^{\circ}C$		1,2 (0,9)	1,4 (1,1)	V
r <sub>T</sub>	T <sub>j</sub> = 25 (125) °C		1,7 (3)	1,7 (3)	mΩ
I <sub>RRM</sub>	I <sub>F</sub> = 300 A; T <sub>j</sub> = 125 ( ) °C di/dt = 5800 A/µs		418 117		A
Q <sub>rr</sub>					μC
E <sub>rr</sub>	V <sub>GE</sub> = 0 V		78		mJ
	characteristics	i		0.075	1.000
R <sub>th(j-c)</sub>	per IGBT			0,075	K/W
R <sub>th(j-c)D</sub>	per Inverse Diode per FWD			0,125 0,125	K/W K/W
R <sub>th(j-c)FD</sub>					
R <sub>th(c-s)</sub>	per module			0,038	K/W
Mechanio	•	1 6		-	Lau
M <sub>s</sub>	to heatsink M6	3		5	Nm
M <sub>t</sub>	to terminals M6	2,5		5	Nm
W				325	g

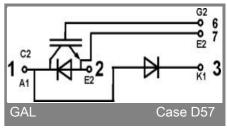


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This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, Chapter IX.

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