

QM50TB-H Transistors

Darlington Three-Phase Bridge

Isolated Case (Y/N) Yes

Circuits Per Package 1

$V_{(BR)CEO}$ (V) 600°

$V_{(BR)CBO}$ (V) 600

$I_{(C)}$ Max. (A) 50

Absolute Max. Power Diss. (W) 310

Maximum Operating Temp (°C) 150°

$h_{(FE)}$ Min. Current gain. 75

@ $I_{(C)}$ (A) (Test Condition) 50

@ V_{CE} (test) 2.0

$V_{(CE)sat}$ Max. (V) 2.0

@ $I_{(C)}$ (A) (Test Condition) 50

@ $I_{(B)}$ (A) (Test Condition)

$t_{(r)}$ Max. (s) Rise time 1.5u®

$t_{(f)}$ Max. (s) Fall time. 15uÀ

Package Style Module-s/q

Pinout Equivalence Code 17-3

Ckt. (Pinout) Number TR01700003

Description