TOSHIBA MG200Q1US51

TOSHIBA GTR MODULE SILICON N CHANNEL IGBT

MG200Q1US51

HIGH POWER SWITCHING APPLICATIONS

MOTOR CONTROL APPLICATIONS

• High Input Impedance

• High Speed : $t_f = 0.3 \mu s$ (Max.)

@Inductive Load

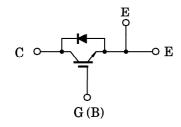
• Low Saturation Voltage

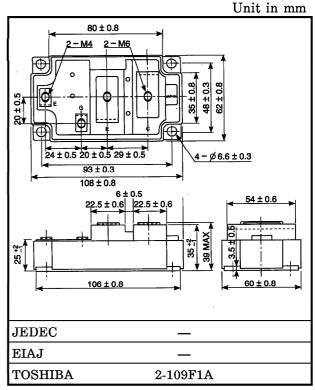
: $V_{CE (sat)} = 3.6V (Max.)$

• Enhancement-Mode

• The Electrodes are Isolated from Case.

EQUIVALENT CIRCUIT





Weight: 465g

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Collector-Emitter Voltage		$v_{\rm CES}$	1200	V	
Gate-Emitter Voltage	v_{GES}	±20	V		
Collector Current	DC	I _C (25°C/80°C)	300 / 200	A	
	1ms	I _{CP} (25°C/80°C)	600 / 400	A	
Forward Current	DC	$I_{\mathbf{F}}$	200	A	
Forward Current	1ms	I_{FM}	400		
Collector Power Dissipation (Tc=25°	PC	1500	W		
Junction Temperature	T _j 150		°C		
Storage Temperature Range		$ m T_{stg}$	-40~125	°C	
Isolation Voltage	$ m V_{Isol}$	2500 (AC 1 minute)	V		
Screw Torque (Terminal: M4/M6/Mounting)		_ 2/3/3		N∙m	

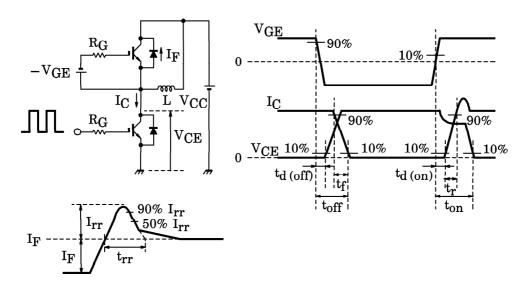
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ELECTRICAL	CHARACTERISTICS	$(Ta = 25^{\circ}C)$
		114 - 23 C/

СНА	RACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leak	age Current	$I_{ ext{GES}}$	$V_{GE} = \pm 20V, V_{CE} = 0$		_	±500	nA
Collector (Cut-off Current	ICES	$V_{CE} = 1200V, V_{GE} = 0$	_	_	4.0	mA
Gate-Emit	ter Cut-off Voltage	V _{GE (off)}	$I_C=200$ mA, $V_{CE}=5$ V	3.0	_	6.0	V
Collector-E	Emitter	Van	$I_C = 200A$, $T_j = 25$ °C	_	2.8	3.6	V
Saturation	Voltage	V _{CE} (sat)	$V_{GE} = 15V$ $T_j = 125^{\circ}C$	_	3.1	4.0]
Input Cap	acitance	$\mathrm{c}_{\mathrm{ies}}$	$V_{CE} = 10V, V_{GE} = 0, f = 1MHz$	—	24.0	-	nF
Switching Time	Turn-on Delay Time	^t d (on)	Inductive Load	-	0.05	-	
	Rise Time	$t_{\mathbf{r}}$	$V_{\rm CC} = 600 V$	_	0.05	_	
	Turn-on Time	t_{on}	$I_{\rm C}$ =200A		0.2	_]
	Turn-off Delay Time	td (off)	$V_{GE} = \pm 15V$	_	0.5	_	μ s
	Fall Time	t_f	$R_{G}=4.7\Omega$	_	0.1	0.3	
	Turn-off Time	$t_{ m off}$	(Note 1)	_	0.6	_	
Forward V	oltage	$V_{\mathbf{F}}$	$I_{F} = 200A, V_{GE} = 0$	_	2.4	3.5	V
Description Description	t _{rr}	$I_F = 200A, V_{GE} = -10V$		_ 0.15	0.3	, ,,,	
Reverse Recovery Time		$di/dt = 700A/\mu s \qquad (Note 1)$	-	0.15	0.3	μ s	
Thermal Resistance		R _{th (j-c)}	Transistor Stage		_	0.08	°C/W
			Diode Stage	_	_	0.24	

(Note 1) Switching Time and Reverse Recovery Time Test Circuit & Timing Chart



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