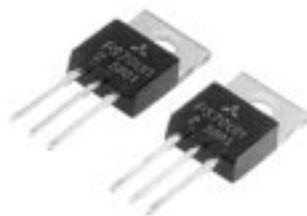


**FS70UM-2**

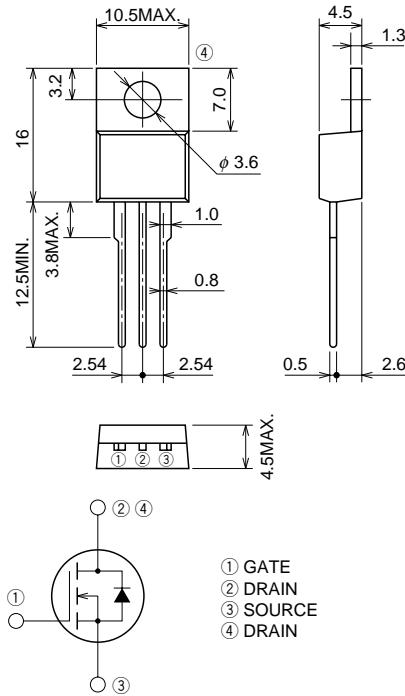
HIGH-SPEED SWITCHING USE

**FS70UM-2**

- 10V DRIVE
- V<sub>DSS</sub> ..... 100V
- r<sub>D(S)</sub> (ON) (MAX) ..... 20mΩ
- I<sub>D</sub> ..... 70A
- Integrated Fast Recovery Diode (TYP.) ..... 120ns

**OUTLINE DRAWING**

Dimensions in mm

**APPLICATION**

Motor control, Lamp control, Solenoid control  
DC-DC converter, etc.

**MAXIMUM RATINGS** ( $T_c = 25^\circ\text{C}$ )

Symbol	Parameter	Conditions	Ratings	Unit
V <sub>DSS</sub>	Drain-source voltage	V <sub>GS</sub> = 0V	100	V
V <sub>GSS</sub>	Gate-source voltage	V <sub>DS</sub> = 0V	±20	V
I <sub>D</sub>	Drain current		70	A
I <sub>DM</sub>	Drain current (Pulsed)		280	A
I <sub>DA</sub>	Avalanche drain current (Pulsed)	L = 100μH	70	A
I <sub>S</sub>	Source current		70	A
I <sub>SM</sub>	Source current (Pulsed)		280	A
P <sub>D</sub>	Maximum power dissipation		125	W
T <sub>ch</sub>	Channel temperature		-55 ~ +150	°C
T <sub>stg</sub>	Storage temperature		-55 ~ +150	°C
—	Weight	Typical value	2.0	g

**HIGH-SPEED SWITCHING USE****ELECTRICAL CHARACTERISTICS** ( $T_{ch} = 25^{\circ}\text{C}$ )

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
$V_{(BR) DSS}$	Drain-source breakdown voltage	$ID = 1\text{mA}$ , $V_{GS} = 0\text{V}$	100	—	—	V
$I_{GSS}$	Gate-source leakage current	$V_{GS} = \pm 20\text{V}$ , $V_{DS} = 0\text{V}$	—	—	$\pm 0.1$	$\mu\text{A}$
$I_{DSS}$	Drain-source leakage current	$V_{DS} = 100\text{V}$ , $V_{GS} = 0\text{V}$	—	—	0.1	mA
$V_{GS(\text{th})}$	Gate-source threshold voltage	$ID = 1\text{mA}$ , $V_{DS} = 10\text{V}$	2.0	3.0	4.0	V
$r_{DS(\text{ON})}$	Drain-source on-state resistance	$ID = 35\text{A}$ , $V_{GS} = 10\text{V}$	—	14	20	$\text{m}\Omega$
$V_{DS(\text{ON})}$	Drain-source on-state voltage	$ID = 35\text{A}$ , $V_{GS} = 10\text{V}$	—	0.49	0.7	V
$ y_{fs} $	Forward transfer admittance	$ID = 35\text{A}$ , $V_{DS} = 10\text{V}$	—	53	—	S
$C_{iss}$	Input capacitance	$V_{DS} = 10\text{V}$ , $V_{GS} = 0\text{V}$ , $f = 1\text{MHz}$	—	6540	—	pF
$C_{oss}$	Output capacitance		—	1150	—	pF
$C_{rss}$	Reverse transfer capacitance		—	500	—	pF
$t_{d(\text{on})}$	Turn-on delay time	$V_{DD} = 50\text{V}$ , $ID = 35\text{A}$ , $V_{GS} = 10\text{V}$ , $R_{GEN} = R_{GS} = 50\Omega$	—	95	—	ns
$t_r$	Rise time		—	175	—	ns
$t_{d(\text{off})}$	Turn-off delay time		—	330	—	ns
$t_f$	Fall time		—	190	—	ns
$V_{SD}$	Source-drain voltage	$Is = 35\text{A}$ , $V_{GS} = 0\text{V}$	—	1.0	1.5	V
$R_{th(\text{ch-c})}$	Thermal resistance	Channel to case	—	—	1.00	$^{\circ}\text{C}/\text{W}$
$t_{rr}$	Reverse recovery time	$Is = 70\text{A}$ , $dIs/dt = -100\text{A}/\mu\text{s}$	—	120	—	ns

**PERFORMANCE CURVES**