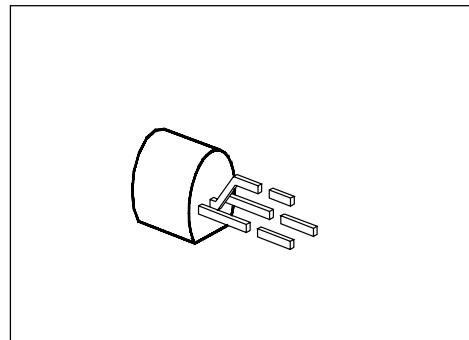


NPN Silicon Darlington Transistors

BC 875
... BC 879

- High current gain
- Low collector-emitter saturation voltage
- Complementary types: BC 876, BC 878
BC 880 (PNP)



Type	Marking	Ordering Code	Pin Configuration			Package ¹⁾
			1	2	3	
BC 875	–	C62702-C853	E	C	B	TO-92
BC 877		C62702-C854				
BC 879		C62702-C855				

Maximum Ratings

Parameter	Symbol	Values	Unit		
			BC 875	BC 877	BC 879
Collector-emitter voltage	V_{CE0}	45	60	80	V
Collector-base voltage	V_{CB0}	60	80	100	
Emitter-base voltage	V_{EB0}		5		
Collector current	I_C		1		A
Peak collector current	I_{CM}		2		
Base current	I_B		100		
Peak base current	I_{BM}		200		mA
Total power dissipation, $T_C = 90^\circ\text{C}$ ²⁾	P_{tot}		0.8 (1)		
Junction temperature	T_j		150		
Storage temperature range	T_{stg}		– 65 ... + 150		

Thermal Resistance

Junction - ambient ²⁾	$R_{th JA}$	≤ 156	K/W
Junction - case ³⁾	$R_{th JC}$	≤ 75	

¹⁾ For detailed information see chapter Package Outlines.

²⁾ If transistors with max. 4 mm lead length are fixed on PCBs with a min. 10 mm × 10 mm large copper area for the collector terminal, $R_{th JA} = 125 \text{ K/W}$ and thus $P_{tot \max} = 1 \text{ W}$ at $T_A = 25^\circ\text{C}$.

³⁾ Mounted on Al heat sink 15 mm × 25 mm × 0.5 mm.

Electrical Characteristicsat $T_A = 25^\circ\text{C}$, unless otherwise specified.

Parameter	Symbol	Values			Unit
		min.	typ.	max.	

DC characteristics

Collector-emitter breakdown voltage $I_C = 50 \text{ mA}$	$V_{(\text{BR})\text{CE}0}$				V
BC 875		45	—	—	
BC 877		60	—	—	
BC 879		80	—	—	
Collector-base breakdown voltage $I_C = 100 \mu\text{A}$	$V_{(\text{BR})\text{CB}0}$				
BC 875		60	—	—	
BC 877		80	—	—	
BC 879		100	—	—	
Emitter-base breakdown voltage, $I_E = 100 \mu\text{A}$	$V_{(\text{BR})\text{EB}0}$	5	—	—	
Collector cutoff current $V_{\text{CE}} = 0.5 \times V_{\text{CEmax}}$	$I_{\text{CE}0}$	—	—	500	nA
Collector cutoff current $V_{\text{CB}} = V_{\text{CBmax}}$ $V_{\text{CB}} = V_{\text{CBmax}}, T_A = 150^\circ\text{C}$	$I_{\text{CB}0}$	—	—	100	nA
—		—	—	20	μA
Emitter cutoff current, $V_{\text{EB}} = 4 \text{ V}$	$I_{\text{EB}0}$	—	—	100	nA
DC current gain $I_C = 150 \text{ mA}; V_{\text{CE}} = 10 \text{ V}^1)$ $I_C = 500 \text{ mA}; V_{\text{CE}} = 10 \text{ V}^1)$	h_{FE}	1000	—	—	—
—		2000	—	—	
Collector-emitter saturation voltage ¹⁾ $I_C = 500 \text{ mA}, I_B = 0.5 \text{ mA}$ $I_C = 1 \text{ A}, I_B = 1 \text{ mA}$	V_{CESat}	—	—	1.3	V
—		—	—	1.8	
Base-emitter saturation voltage ¹⁾ $I_C = 1 \text{ A}; I_B = 1 \text{ mA}$	V_{BESat}	—	—	2.2	

AC characteristics

Transition frequency $I_C = 200 \text{ mA}, V_{\text{CE}} = 5 \text{ V}, f = 20 \text{ MHz}$	f	—	150	—	MHz
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¹⁾ Pulse test: $t \leq 300 \mu\text{s}$, $D \leq 2 \%$.