

IGBT MODULE (S series)

600V / 30A / PIM



■ Features

- Low V_{ce(sat)}
- Compact package
- P.C. board mount
- Converter diode bridge, Dynamic brake circuit

■ Applications

- Inverter for motor drive
- AC and DC servo drive amplifier
- Uninterruptible power supply

■ Maximum ratings and characteristics

● Absolute maximum ratings (T_c=25°C unless without specified)

Item	Symbol	Condition	Rating	Unit
Inverter	Collector-Emitter voltage	V _{CES}	600	V
	Gate-Emitter voltage	V _{GES}	±20	V
	Collector current	I _c	Continuous	30
		I _{CP}	1ms	60
		-I _c		30
	Collector power dissipation	P _c	1 device	W
Brake	Collector-Emitter voltage	V _{CES}	600	V
	Gate-Emitter voltage	V _{GES}	±20	V
	Collector current	I _c	Continuous	20
		I _{CP}	1ms	40
	Collector power dissipation	P _c	1 device	W
	Repetitive peak reverse voltage	V _{RRM}		V
Converter	Repetitive peak reverse voltage	V _{RRM}	800	V
	Average output current	I _o	50Hz/60Hz sine wave	A
	Surge current (Non-Repetitive)	I _{FSM}	T _j =150°C, 10ms half sine wave	210
	I ² t (Non-Repetitive)	I ² t		221
	Operating junction temperature	T _j		°C
	Storage temperature	T _{stg}		°C
Isolation between terminal and copper base *2	V _{iso}	AC : 1 minute	AC 2500	V
	voltage between thermistor and others *3		AC 2500	V
			3.5 *1	N·m
Mounting screw torque				

*1 Recommendable value : 2.5 to 3.5 N·m (M5)

*2 All terminals should be connected together when isolation test will be done.

*3 Terminal 8 and 9 should be connected together. Terminal 1 to 7 and 10 to 24 should be connected together and shorted to copper base.

● Electrical characteristics ($T_j=25^\circ\text{C}$ unless otherwise specified)

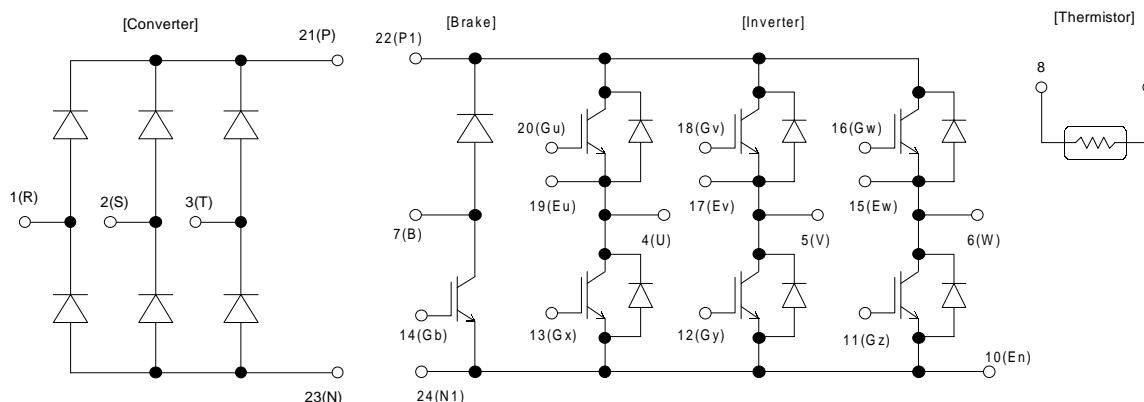
Item	Symbol	Condition	Characteristics			Unit
			Min.	Typ.	Max.	
Inverter	Zero gate voltage collector current	I_{CES}	$V_{CE}=600\text{V}, V_{GE}=0\text{V}$			1.0 mA
	Gate-Emitter leakage current	I_{GES}	$V_{CE}=0\text{V}, V_{GE}=\pm 20\text{V}$			0.2 μA
	Gate-Emitter threshold voltage	$V_{GE(\text{th})}$	$V_{CE}=20\text{V}, I_c=30\text{mA}$	5.5	7.8	8.5 V
	Collector-Emitter saturation voltage	$V_{CE(\text{sat})}$	$V_{GE}=15\text{V}, I_c=30\text{A}$	chip terminal	1.8	
					1.95	2.4 V
	Input capacitance	C_{IES}	$V_{GE}=0\text{V}, V_{CE}=10\text{V}, f=1\text{MHz}$		3000 pF	
	Turn-on time	t_{on}	$V_{CC}=300\text{V}$		0.45	1.2 μs
		t_r	$I_c=30\text{A}$		0.25	0.6
		t_{ri}	$V_{GE}=\pm 15\text{V}$		0.08	
	Turn-off	t_{off}	$R_G=82\Omega$		0.40	1.0
		t_f			0.05	0.35
Brake	Forward on voltage	V_F	$I_F=30\text{A}$	chip terminal	1.8 1.95	2.6 V
	Reverse recovery time of FRD	t_{rr}	$I_F=30\text{A}$			0.3 μs
	Zero gate voltage collector current	I_{CES}	$V_{CES}=600\text{V}, V_{GE}=0\text{V}$			1.0 mA
	Gate-Emitter leakage current	I_{GES}	$V_{CE}=0\text{V}, V_{GE}=\pm 20\text{V}$			0.2 μA
	Collector-Emitter saturation voltage	$V_{CE(\text{sat})}$	$I_c=20\text{A}, V_{GE}=15\text{V}$	chip terminal	1.8 1.95	2.4 V
	Turn-on time	t_{on}	$V_{CC}=300\text{V}$		0.45	1.2 μs
		t_r	$I_c=20\text{A}$		0.25	0.6
	Turn-off time	t_{off}	$V_{GE}=\pm 15\text{V}$		0.40	1.0
		t_f	$R_G=120\Omega$		0.05	0.35
	Reverse current	I_{RRM}	$V_R=600\text{V}$			1.0 mA
Converter	Forward on voltage	V_{FM}	$I_F=30\text{A}$	chip terminal	1.1 1.2	1.5 V
	Reverse current	I_{RRM}	$V_R=800\text{V}$			1.0 mA
Thermistor	Resistance	R	$T=25^\circ\text{C}$		5000	
			$T=100^\circ\text{C}$		465	495
	B value	B	$T=25/50^\circ\text{C}$		3305	3375
					3450	K

● Thermal resistance Characteristics

Item	Symbol	Condition	Characteristics			Unit
			Min.	Typ.	Max.	
Thermal resistance (1 device)	$R_{th(j-c)}$	Inverter IGBT			1.04	$^\circ\text{C/W}$
		Inverter FWD			2.00	
		Brake IGBT			1.56	
		Converter Diode			1.33	
Contact thermal resistance *	$R_{th(c-f)}$	With thermal compound		0.05		

* This is the value which is defined mounting on the additional cooling fin with thermal compound

■ Equivalent Circuit Schematic



■ Outline Drawings, mm

